

e2v

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**EV12DS130**

Spice Models

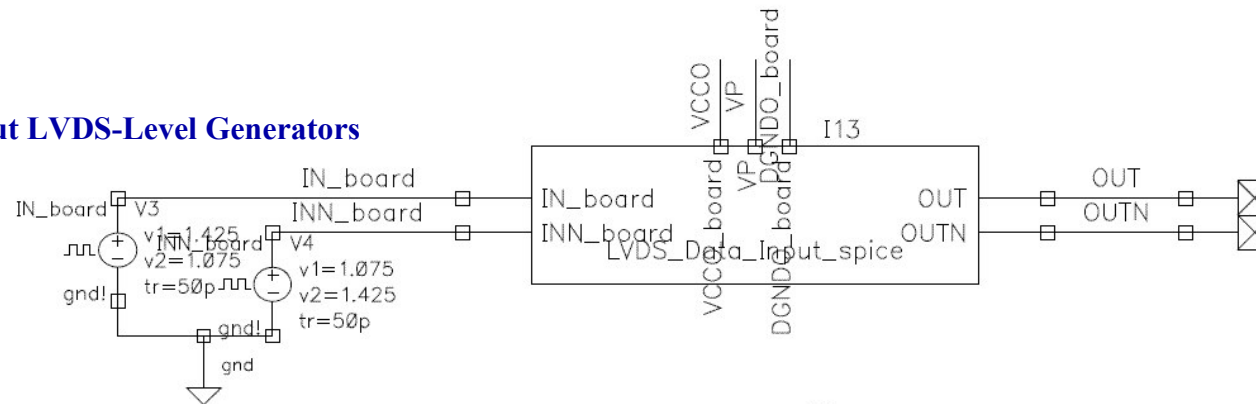
LVDS Data Input Buffer (transient + DC)

# Top Level Simulation Schematic (Transient)

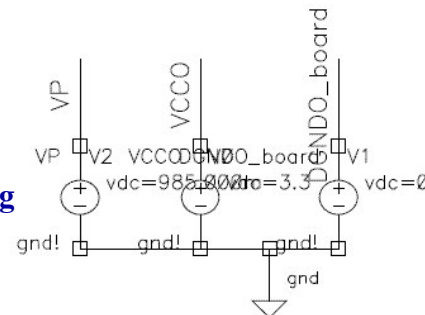
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**LVDS\_Data\_Input\_spice**  
(described hereafter)

**Input LVDS-Level Generators**

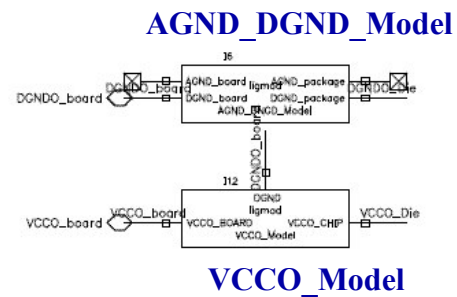
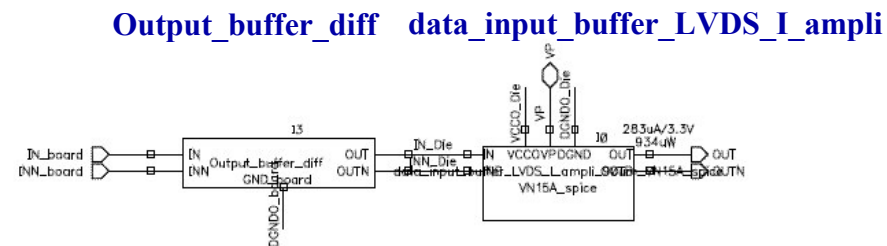


**Biasing**



# TOP LVDS Data Input Schematic

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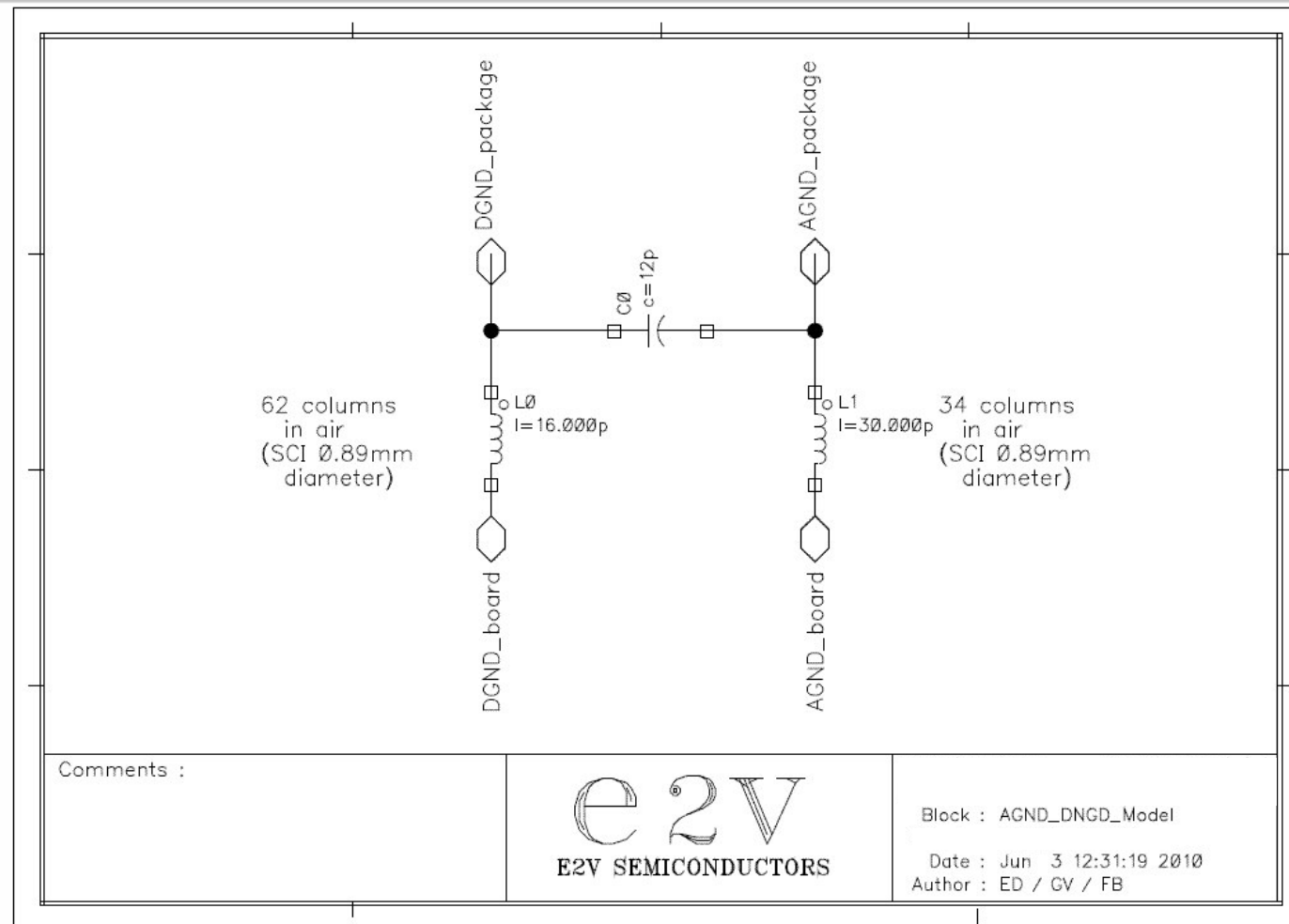
Comments :  
 Data Input Buffer with  
 - Differential Input  
 Transmission Lines  
 - Power Supply and  
 Ground Package Models

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 E2V SEMICONDUCTORS

Block : LVDS\_Data\_Input\_spice  
 Date : Jun 1 10:52:59 2010  
 Author : ED / GV / FB

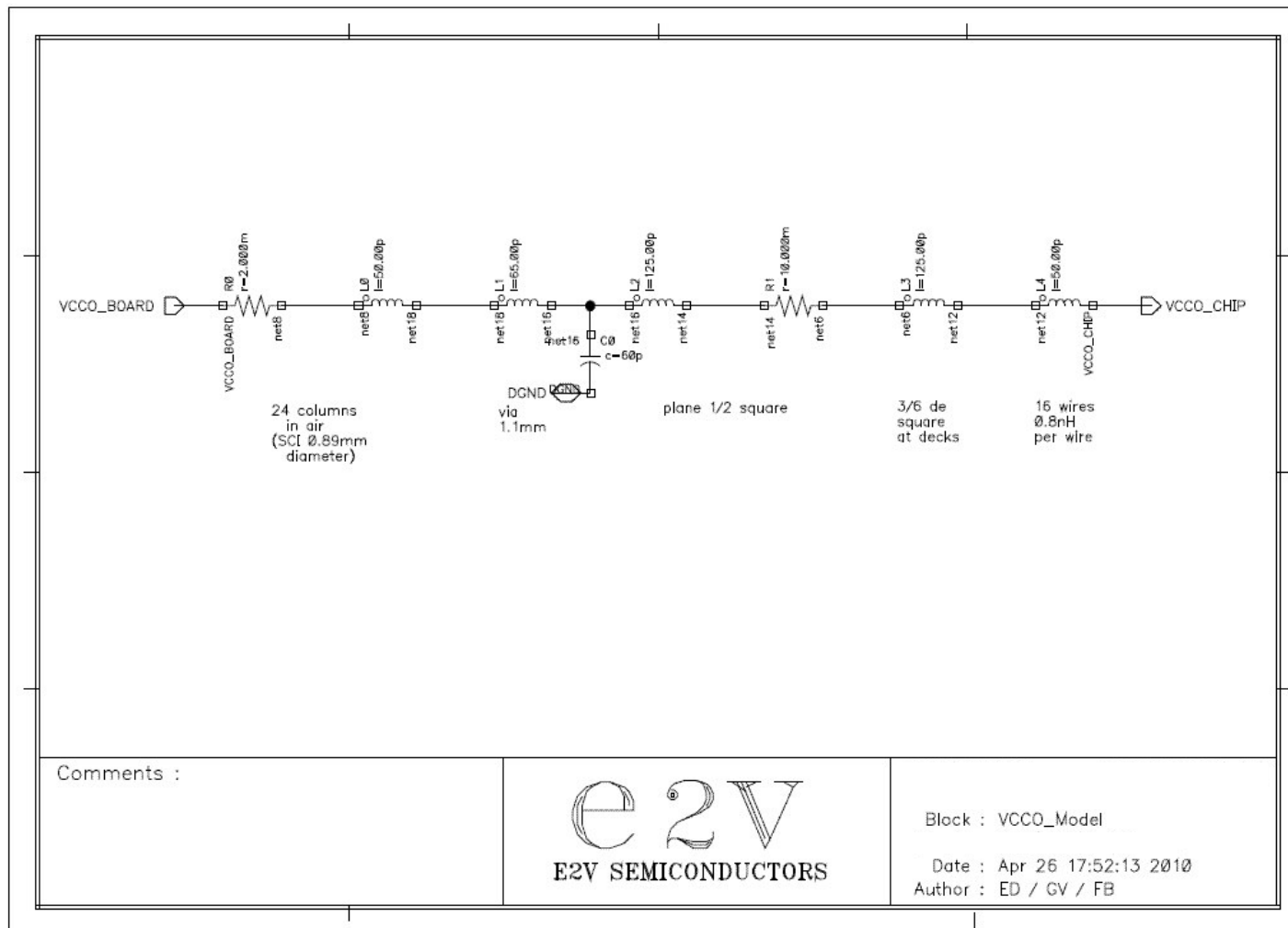
# Ground Package Model AGND & DGND

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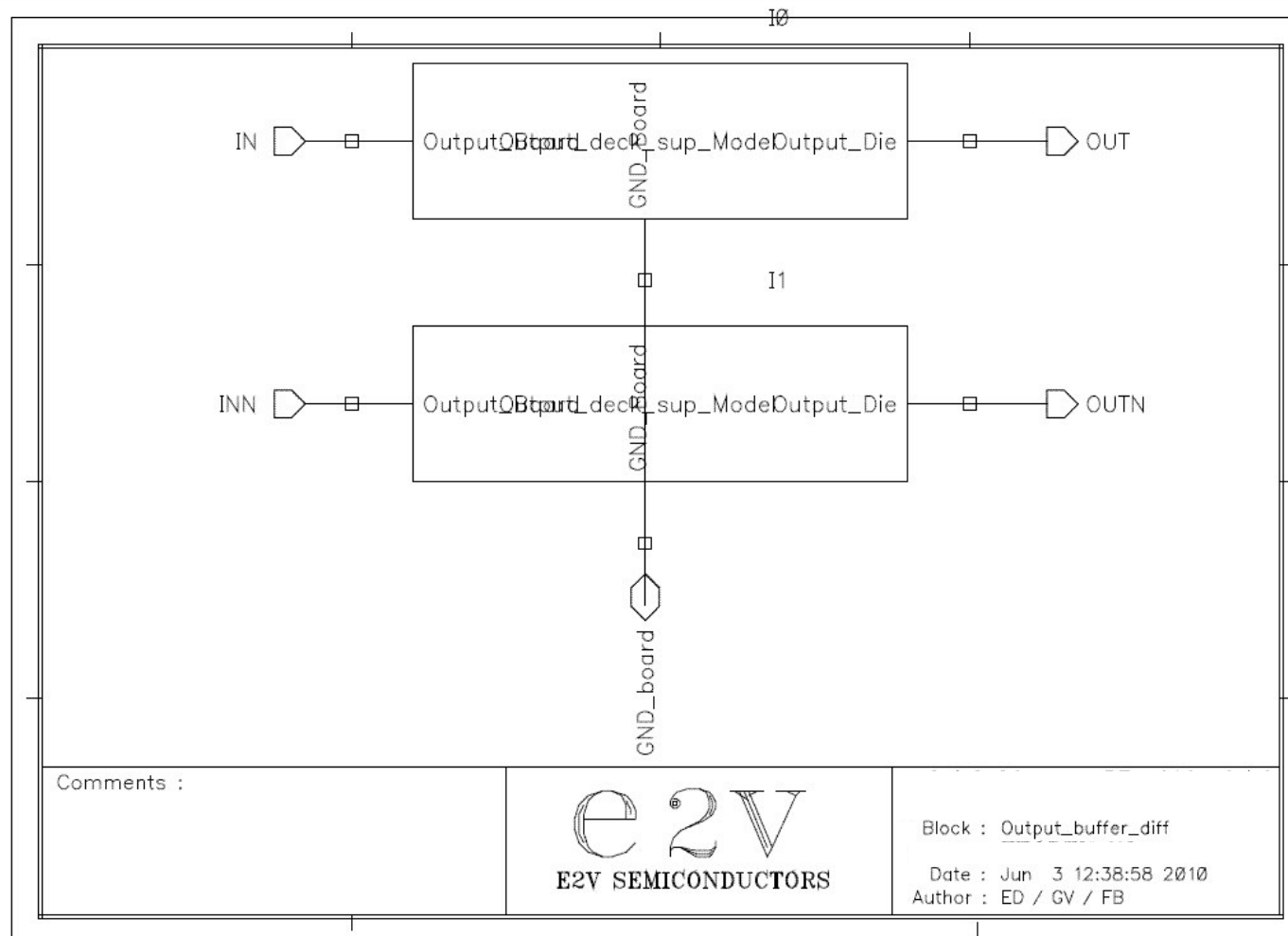
# Power Supply Package Model VCCO

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## I/O Lines Package Models in Differential

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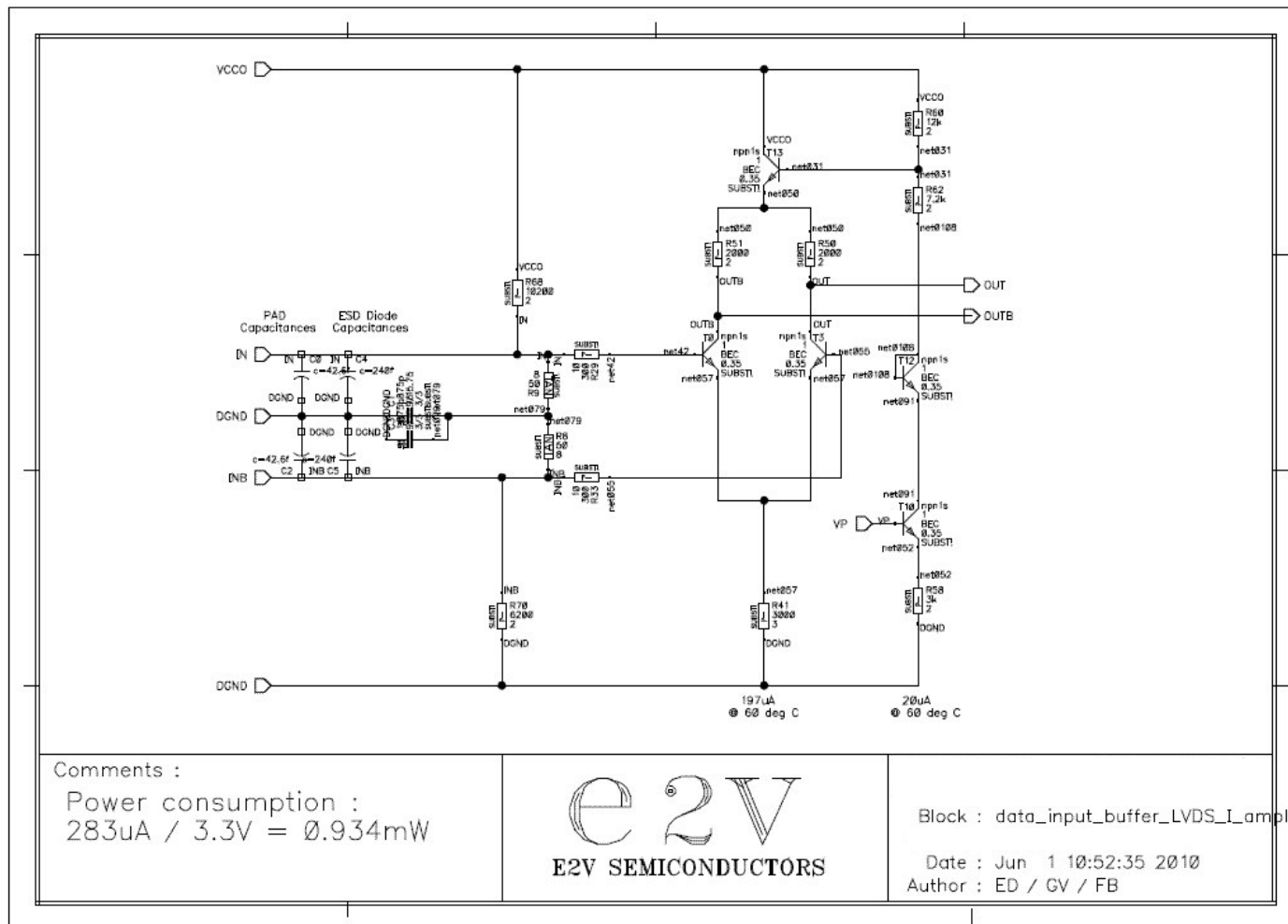


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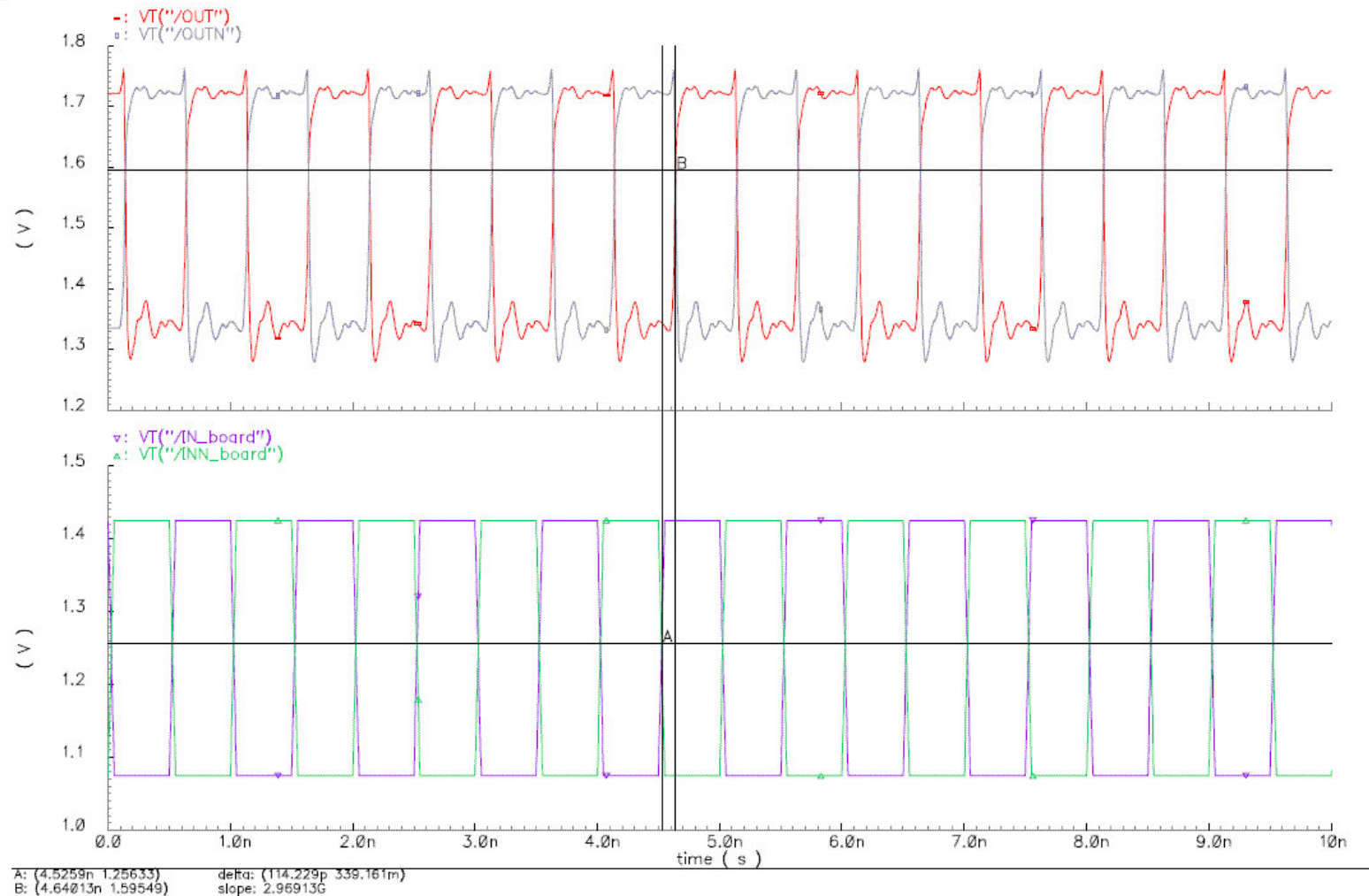
# LVDS Data Input Buffer Schematic

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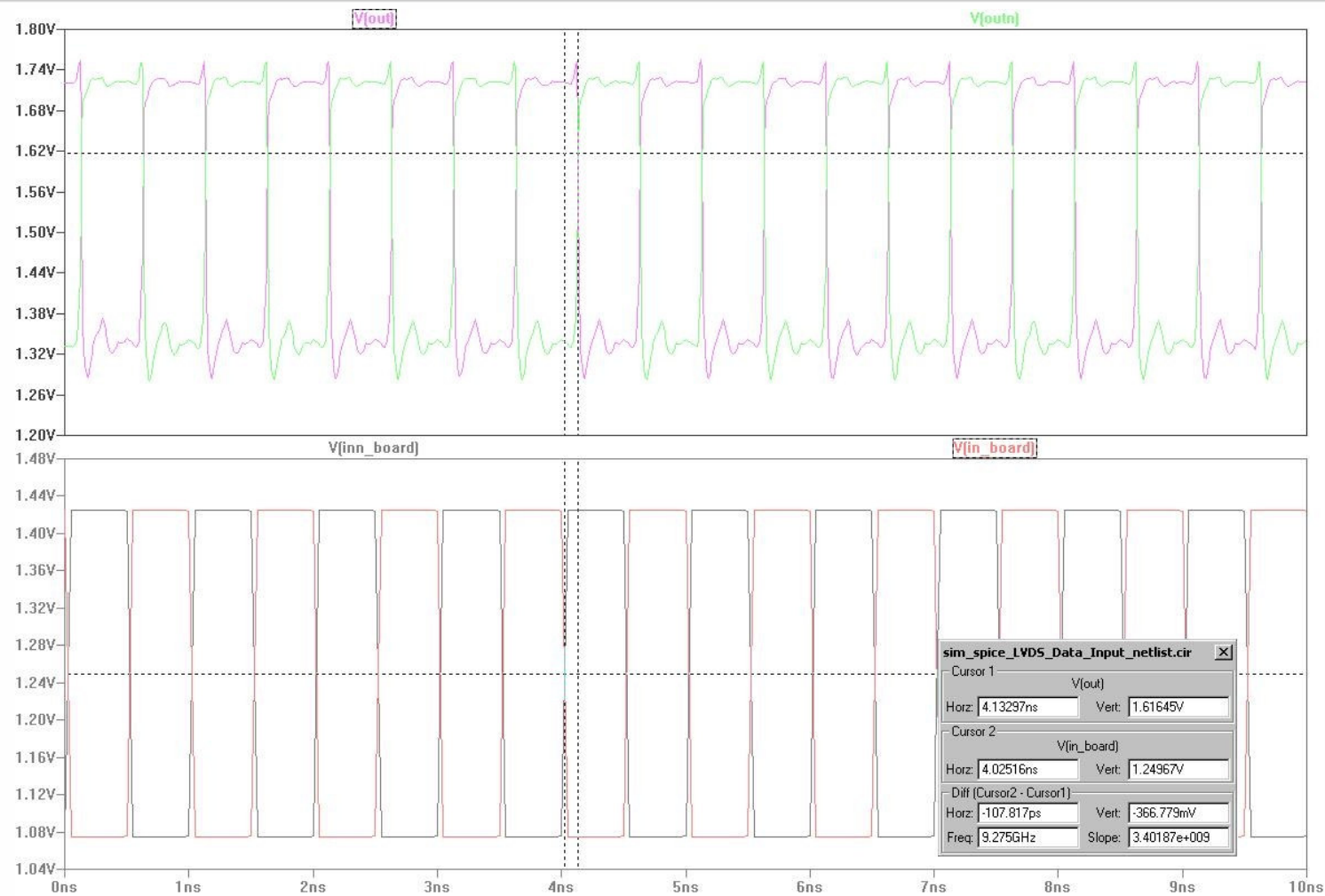
## Data Input Transient Response (Spectre) @27°C; 1GHz

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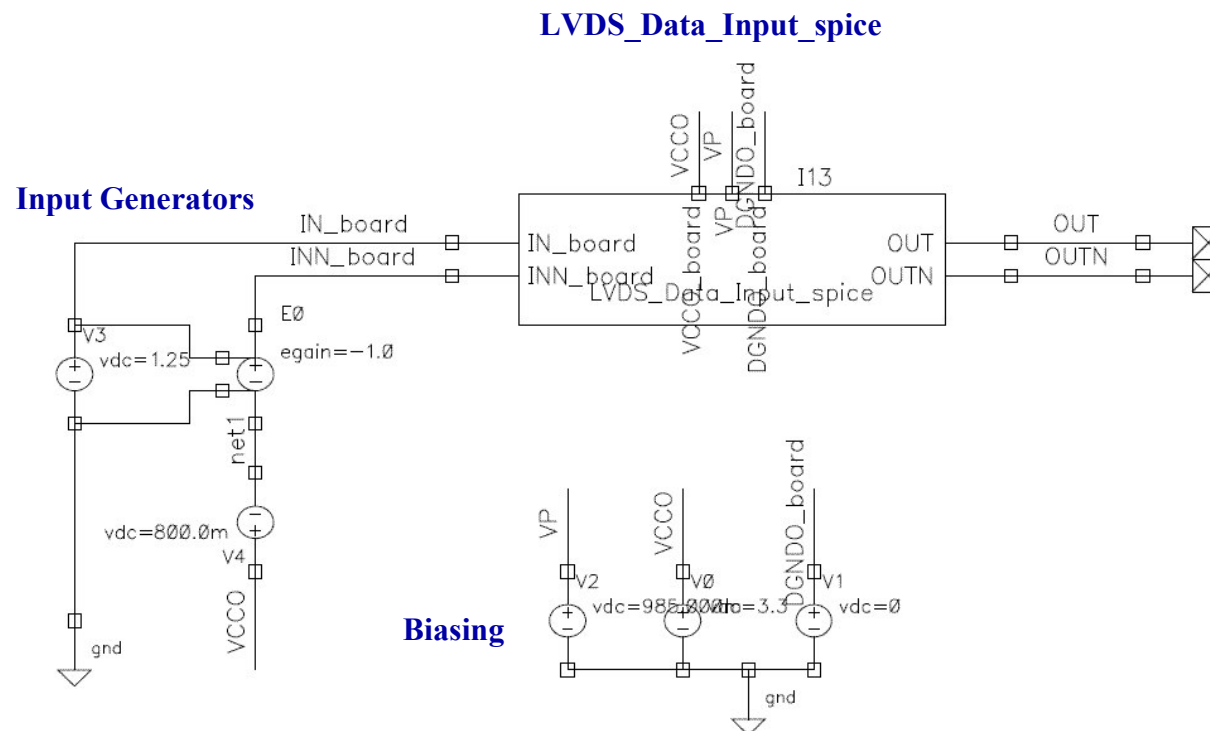
## Data Input Transient Response (Spice) @27°C; 1GHz

e2v



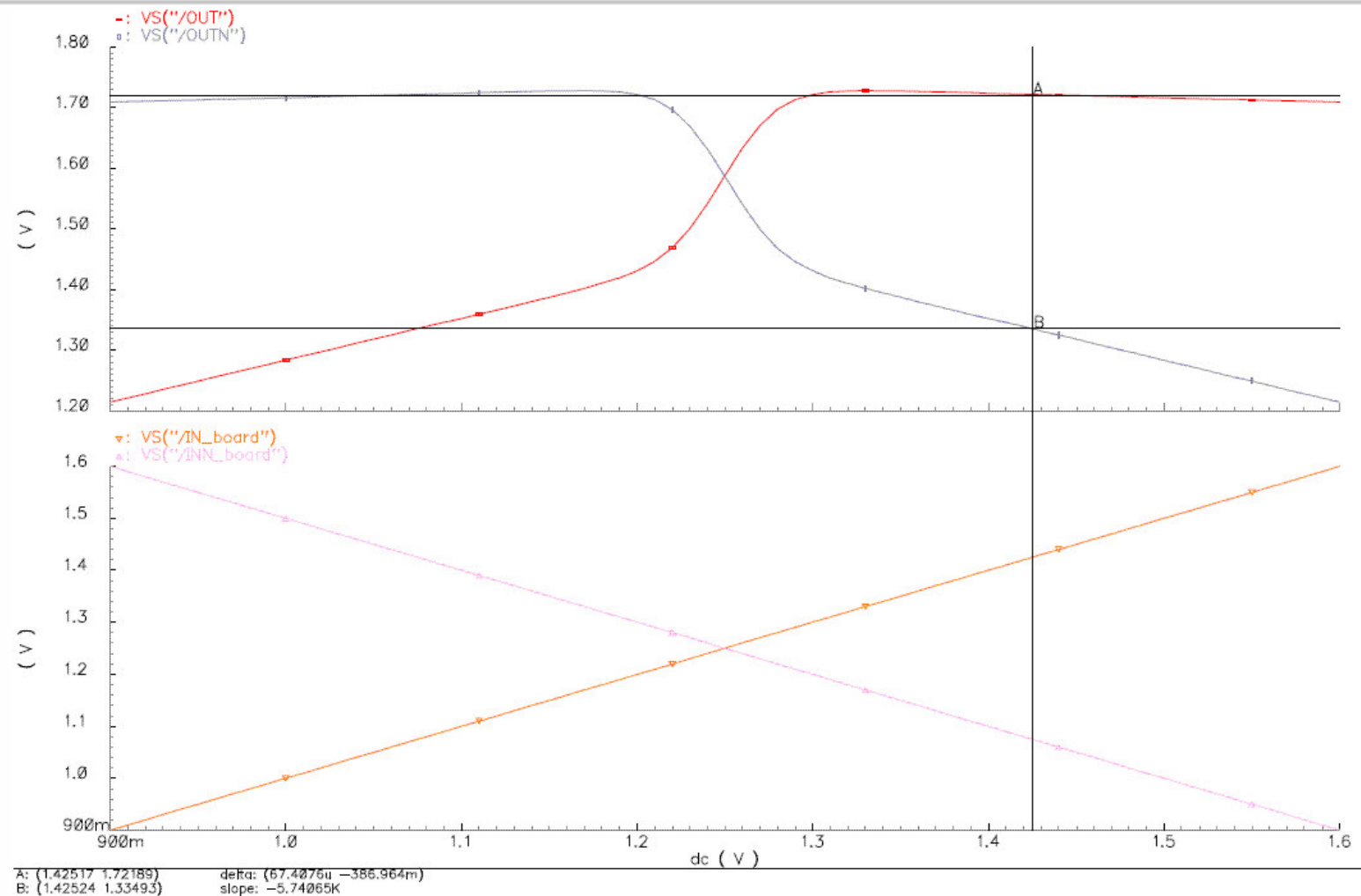
## Top Level Simulation Schematic (DC)

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# Data Input DC Response (Spectre) @27°C; from 0.9V to 1.6V

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## Data Input DC Response (Spice) @27°C; from 0.9V to 1.6V

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